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Patent Assignment Abstract of Title

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Inventors: Steve G. Driediger, John S. Gryba, Charles H. Mitchell

Title: Feature implementation in a real time stamp distribution system

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Exec Dt: 11/22/2000

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Exec Dt: 11/06/2000

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Exec Dt: 11/10/2000

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Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity Data	Foreign Data
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Inventor Name Search Result

Your Search was:

Last Name = DRIEDIGER

First Name = STEVE

Application#	Patent#	Status	Date Filed	Title	Inventor Name 9
<u>10865870</u>	Not Issued	020	06/14/2004	METHOD AND APPARATUS FOR PROVIDING INTEGRAL CELL PAYLOAD INTEGRITY VERIFICATION AND DETECTING DEFECTIVE MODULES IN TELECOMMUNICATION DEVICES	DRIEDIGER, STEVE
<u>10864365</u>	Not Issued	030	06/10/2004	FAIR WRED FOR TCP AND UDP TRAFFIC MIX	DRIEDIGER, STEVE
<u>10717377</u>	Not Issued	030	11/19/2003	METHOD AND APPARATUS FOR DETECTION OF TRANSMISSION UNIT LOSS AND/OR REPLICATION	DRIEDIGER, STEVEN
<u>10663612</u>	Not Issued	030	09/16/2003	FACILITATING ERROR DETECTION FOR CONTENT ADDRESSABLE MEMORY	DRIEDIGER, STEVEN
<u>09636115</u>	Not Issued	041	08/10/2000	METHOD AND APPARATUS FOR CONTROLLING A CLOCK SIGNAL OF A LINE CARD CIRCUIT	DRIEDIGER, STEVEN G.
<u>09635070</u>	Not Issued	071	08/09/2000	FEATURE IMPLEMENTATION IN A REAL TIME STAMP DISTRIBUTION SYSTEM	DRIEDIGER, STEVE G.
<u>09635069</u>	<u>6760764</u>	150	08/09/2000	REAL TIME STAMP DISTRIBUTION	DRIEDIGER, STEVE G.
<u>09476374</u>	<u>6771605</u>	150	01/03/2000	METHOD AND APPARATUS FOR PROVIDING INTEGRAL CELL PAYLOAD INTEGRITY VERIFICATION AND DETECTING DEFECTIVE MODULES IN TELECOMMUNICATION DEVICES	DRIEDIGER, STEVE